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Topology Optimized Photonic Wire Splitters

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Abstract: Photonic wire splitters have been designed using topology optimization. The splitters have been fabricated in silicon-on-insulator material and display broadband low-loss 3 dB splitting in a bandwidth larger than 100 nm.

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1. Introduction

Integrated optical circuits comprising several on-chip functionalities and high component densities demand low-loss routing of optical signals over small areas [1]. In the silicon platform, photonic wires offer the possibility to guide light efficiently with very small losses [2] due to the high index contrast between silicon and the cladding material, which is typically air or silica. Thus, waveguide bends, ring resonators, and splitters can be designed with very compact sizes by using simple design strategies [3]. However, to optimize the performance of ultra compact components new inverse design tools need to be utilized.

Topology optimization [4] has previously been utilized to optimize the performance of different photonic crystal waveguide components [5, 6]. Here, we apply this inverse design method to design a compact, broadband and low-loss photonic wire splitter.

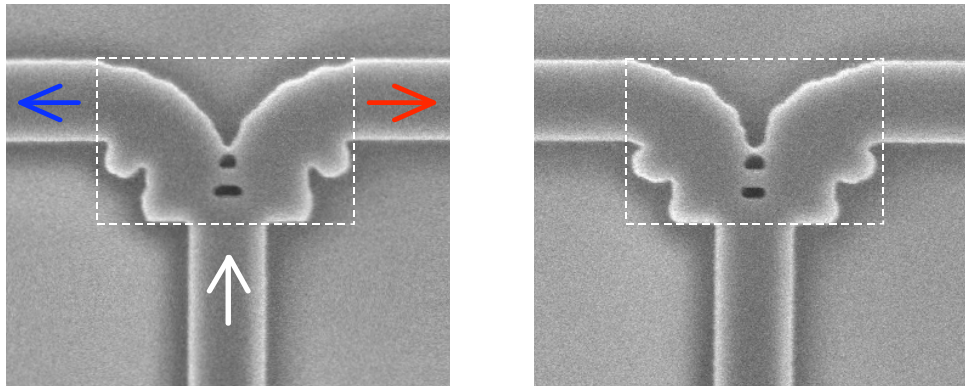


Figure 1. Scanning electron micrographs of the optimized and fabricated photonic wire splitters. The dashed boxes sketch the design areas chosen in the topology optimization.

2. Design and fabrication

The generic photonic wire (PhW) splitter is constructed by combining three PhWs of width 440 nm to form a T-junction. The topology optimization method is applied to a $\sim 1 \mu\text{m}^2$ design area of the T-junction (dashed box in Fig. 1). The silicon material will be redistributed within the design area without any restrictions to the shape and form of the final T-junction. The goal criteria are to optimize the transmission in each of the two output arms and to obtain a low-loss and broadband 3-dB splitting. To test the robustness of the method two different designs have been obtained by choosing different target frequencies in the optimizations. Both designs have been fabricated in a

silicon-on-insulator material by utilizing e-beam lithography and an SF₆-based inductively coupled plasma etch. The thickness of the top silicon layer is ~320 nm and the buried box oxide is 1 μm . Figure 1 show scanning electron micrograph images of the two fabricated PhW splitters. It is seen that the optimized splitters look similar with structured parts on the inner side of the bends and small holes in the middle of the junction.

3. Results and conclusion

The PhW splitters have been characterized optically for the transverse electric mode by using a laser source in the wavelength region from 1480 – 1580 nm. Figure 2 shows the obtained transmission spectra for the splitters shown in Fig. 1, left and right, respectively. The transmission spectra have been normalized to a straight photonic wire of similar width to extract the splitting loss.

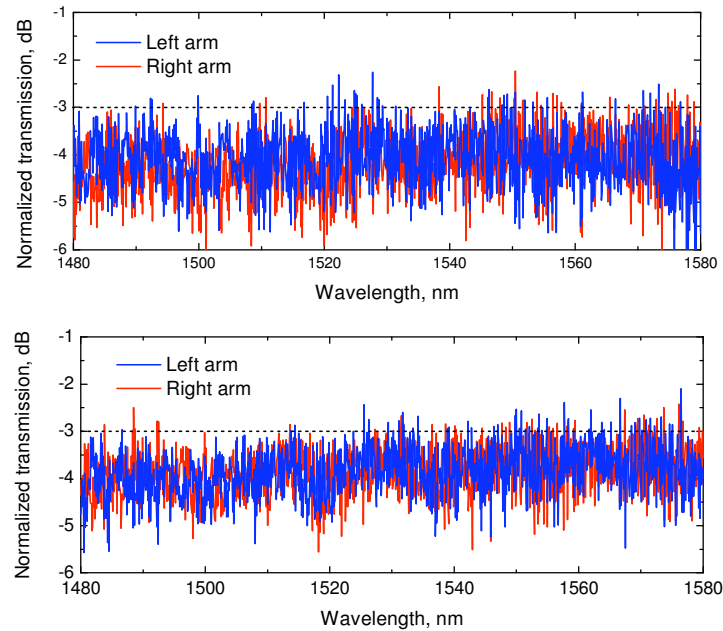


Figure 2. Normalized transmission spectra for the photonic wire splitters shown in Fig. 1, (top) left and (bottom) right splitter.

From the figure it is seen that both optimized splitters split light equivalently to each arms in the investigated wavelength range. The average values of the normalized transmissions are -4.1 ± 0.6 dB and -3.9 ± 0.6 dB over the 100 nm wavelength region for the left and right splitter in Fig. 1, respectively. The excess loss around 1 dB for each splitter is mainly due to roughness in the T-junction. In this way, we have shown that topology optimization can be used to design ultra compact photonic wire splitters with a footprint as small as 1 μm^2 , which perform with low losses over a broad wavelength range. We believe that topology optimization is a strong inverse design tool for realizing compact and optimized photonic crystal and photonic wire components.

4. References

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